FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Attorney Docket No.: MAS-FIN-101 Appl. No.: Applicant: GUNTHER REISSIG Filing Date: November 26, 2003 Group Art Unit:

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
MCJ	A	6,230,101 B1	5/8/01	Wallis			
MCJ	B	6,192,328 B1	2/20/01	Kahlert et al.			
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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES	TRANSL. NO
MCJ	J	09044473 A	2/14/97	Japan				
MCJ	K	01307868 A	12/12/89	Japan				
MCJ	L	99/48030	9/23/99	WIPO				
	M							
	N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

MCJ		Chowdry, S. et al.: "Automatic Structure Analysis of Large Scale Differential Algebraic Systems", IEEE, 2001, pp. 798-803
MCJ		Unger, J. et al.: Structural Analysis of Differential-Algebraic Equation Systems-Theory and Applications", Computers chem.Engng., 1995, pp. 867-882

EXAMINER	DATE CONSIDERED
Mary C. Jacob	11/27/06

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MCJ		Reissig, G. et al.: "Differential-Algebraic Equations of Index 1 May Have an Arbitrarily High Structural Index", SIAM J. SCI. COMPUT., Vol. 21, No. 6, 2000, pp. 1987-1990
MCJ		Lawler, E.: "Combinatorial Optimization, Networks and Matroids", Dover Publications, Inc., 1976, front page, iii, and pp. 192-195

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MCJ		Duff, I. S. et al.: "On Algorithms for Obtaining a Maximum Transversal", ACM Transactions on Mathematical Software, Vol. 7, No. 3, September 1981, pp. 315-330
MCJ		Pantelides, C. C.: "The Consistent Initialization of Differential-Algebraic Systems", SIAM. J. SCI. STAT. COMPUT., Vol. 9, No. 2, March 1988, pp. 213-231;

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		Schultz: "Programm-Dokumentation TITAN-02, Topologie-Test und Row-Swapping Programm-Modul TOPCHK" [Program Documentation TITAN-02, Topology Test and Row Swapping Program Module TOPCHK], Siemens AG ZTI DES 251, July 1988, front page, I, and pp. 1-15
		Röbenack, K. et al.: „Sukzessive Index-Reduktion elektrischer Netzwerke mit Hilfe einer minimalen Anzahl von Zusatzelementen" [Gradual Index Reduction of Electrical Networks with Support of a Minimal Amount of Additional Elements], Gens, W. (ed.), 43rd International Scientific Colloquium, Technical University of Ilmenau, Vol. 3, September 21-24, 1998, pp. 192-197

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MCJ		Reißig, G.: "Extension of the Normal Tree Method", International Journal of Circuit Theory and Applications, Vol. 27, Revised Version, 1999, pp. 241-266
		Feldmann, U. et al.: "Lösbarkeitstest mit Hilfe eines gesonderten Gleichungssystems" [Solubility Test with Help of a Separated System of Equations], Infineon Technologies, MP PTS, January 13, 2000, pp. 1-4

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MEJ		Christen, E. et al.: "Analog and Mixed-Signal Modeling Using the VHDL-AMS Language", 36th Design Automation Conference, VHDL-AMS Tutorial, June 21-25, 1999, pp. 1-198

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